

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Gary L. Swoboda

Serial No: 09/943,603

Filed: August 30, 2001

For: Using Sign Extension to Compress On-Chip Data Processor Trace and Timing  
Information for Export

TI-30491

Art Unit: 2123

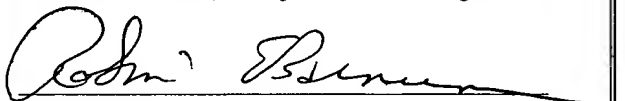
Examiner:

Conf. No.: 2424

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Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith.

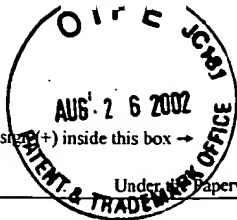
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Respectfully submitted,



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				Application Number	
				Filing Date	August 30, 2001
				First Named Inventor	Gary L. Swoboda
				Group Art Unit	2123
				Examiner Name	
Sheet	1	of	1	Attorney Docket No.	TI-30491

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
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	CA	ARM Limited, RDI 1.5.1tx and RDI 1.5.1rt; Doc. No. RDI-0032-CUST-ESPC-A; 19 May, 2000; pgs. 1-55.	
	CB	ARM Limited, ETM9, Rev. 1, Technical Reference Manual, Doc. No. DDI 0157C, pgs. i - Index-3.	
	CC	ARM Limited, Embedded Trace Macrocell, Rev. 1, Specification, Doc. No. IHI 0014E, pgs. i - Index-3.	
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